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FIG. 1

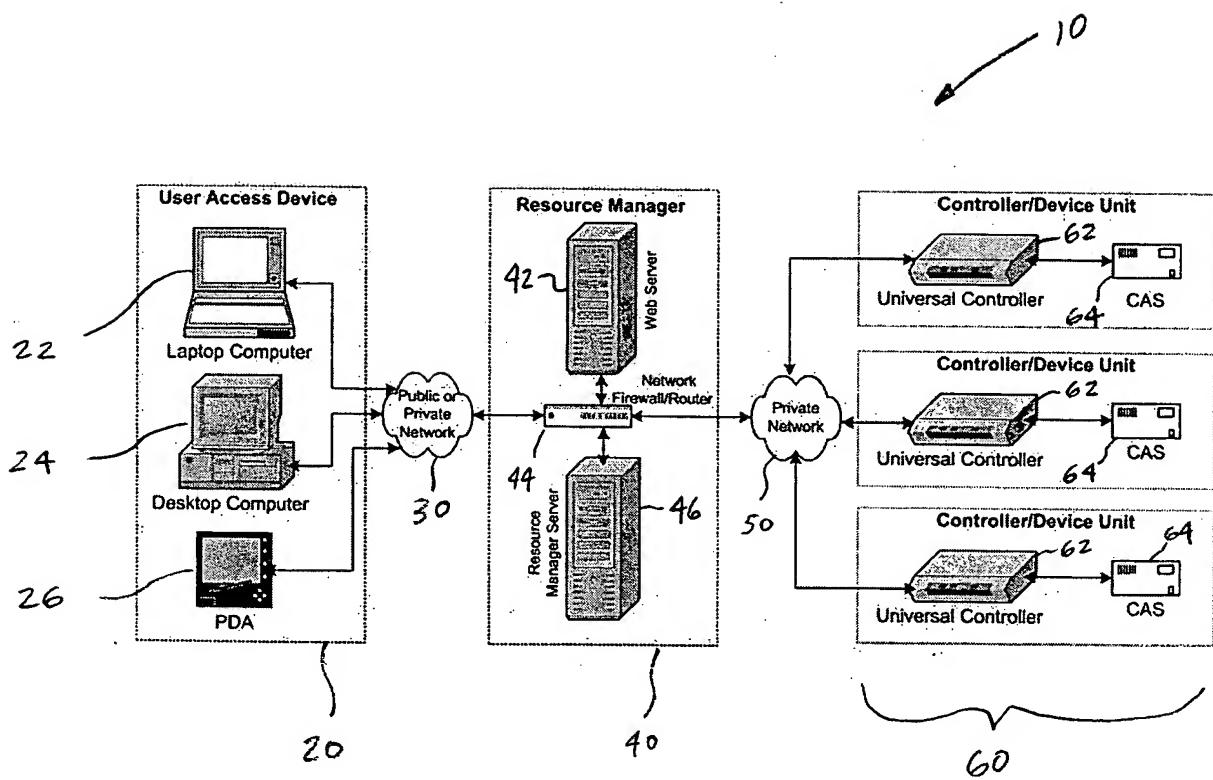


FIG. 2

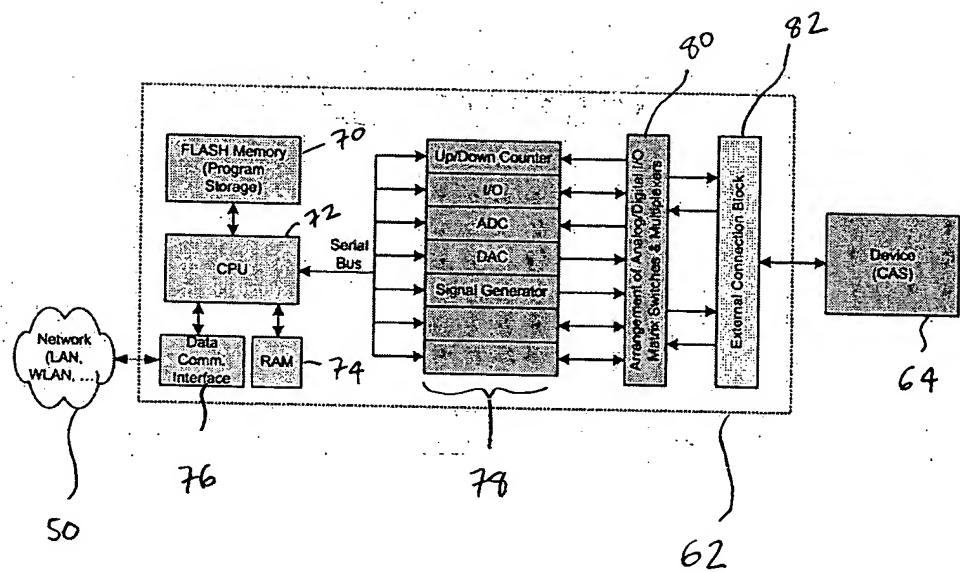


FIG. 3

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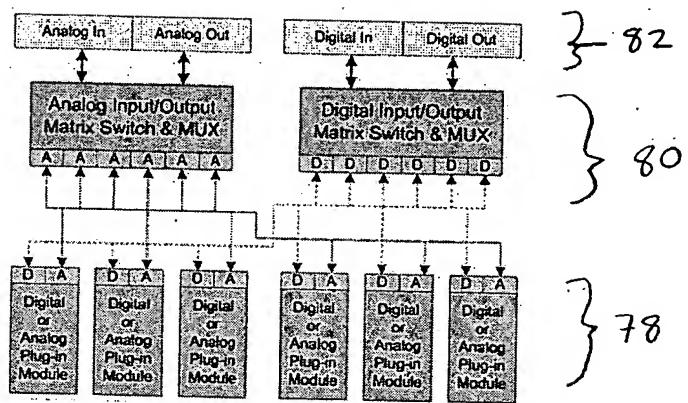


FIG. 4

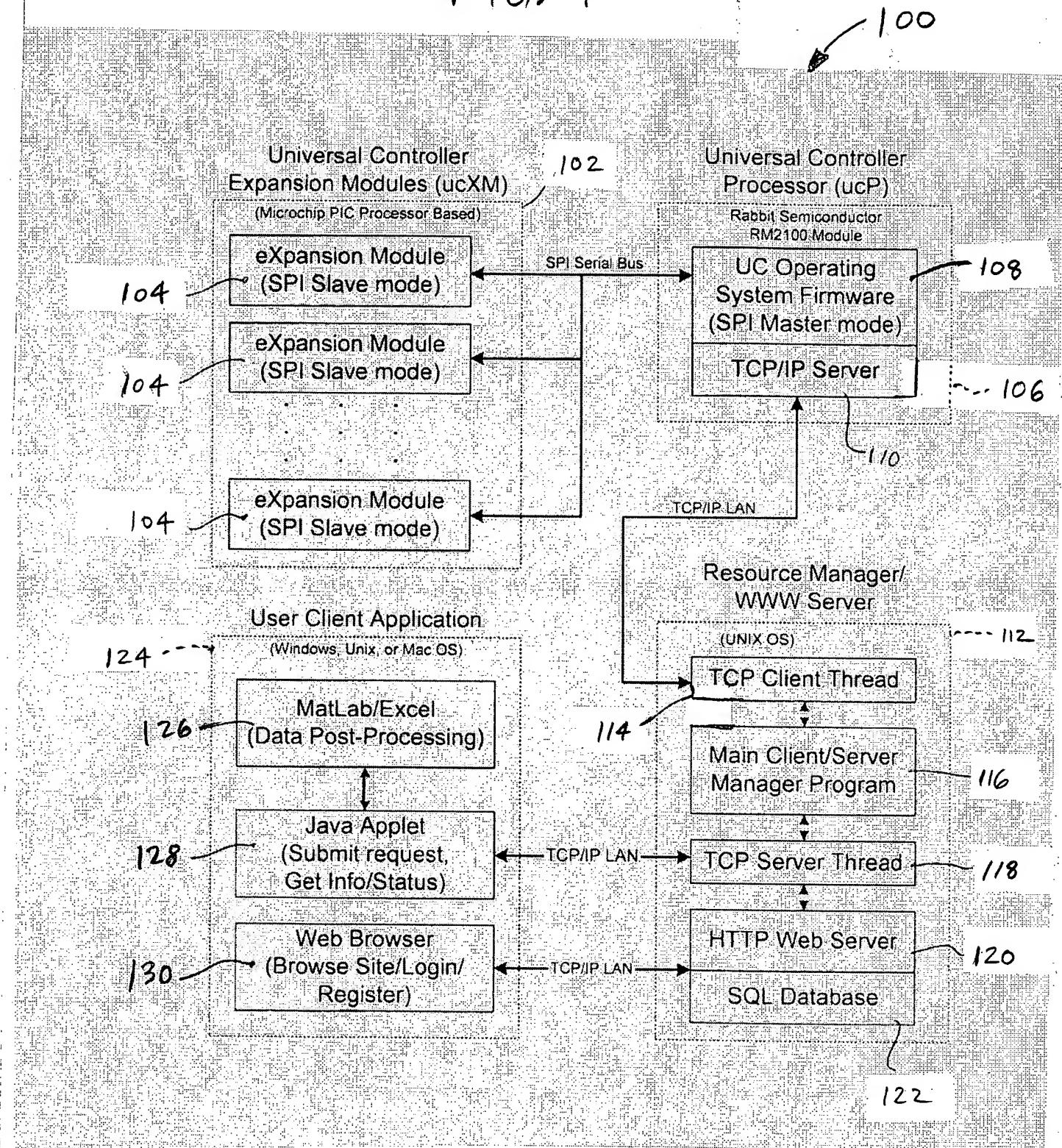


FIG. 5

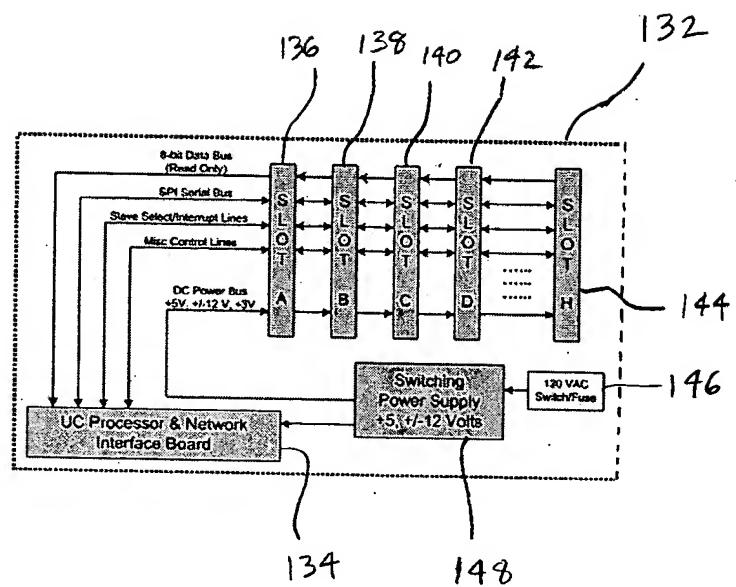


FIG. 6

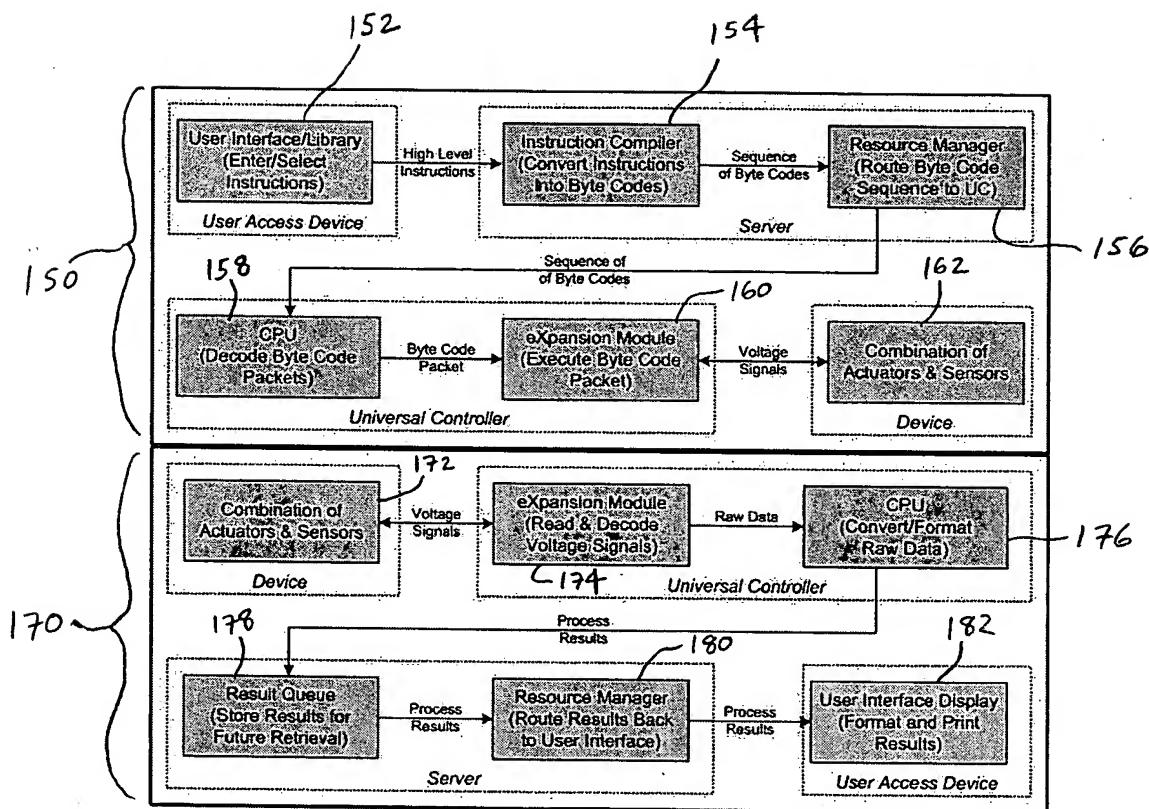


FIG. 7a

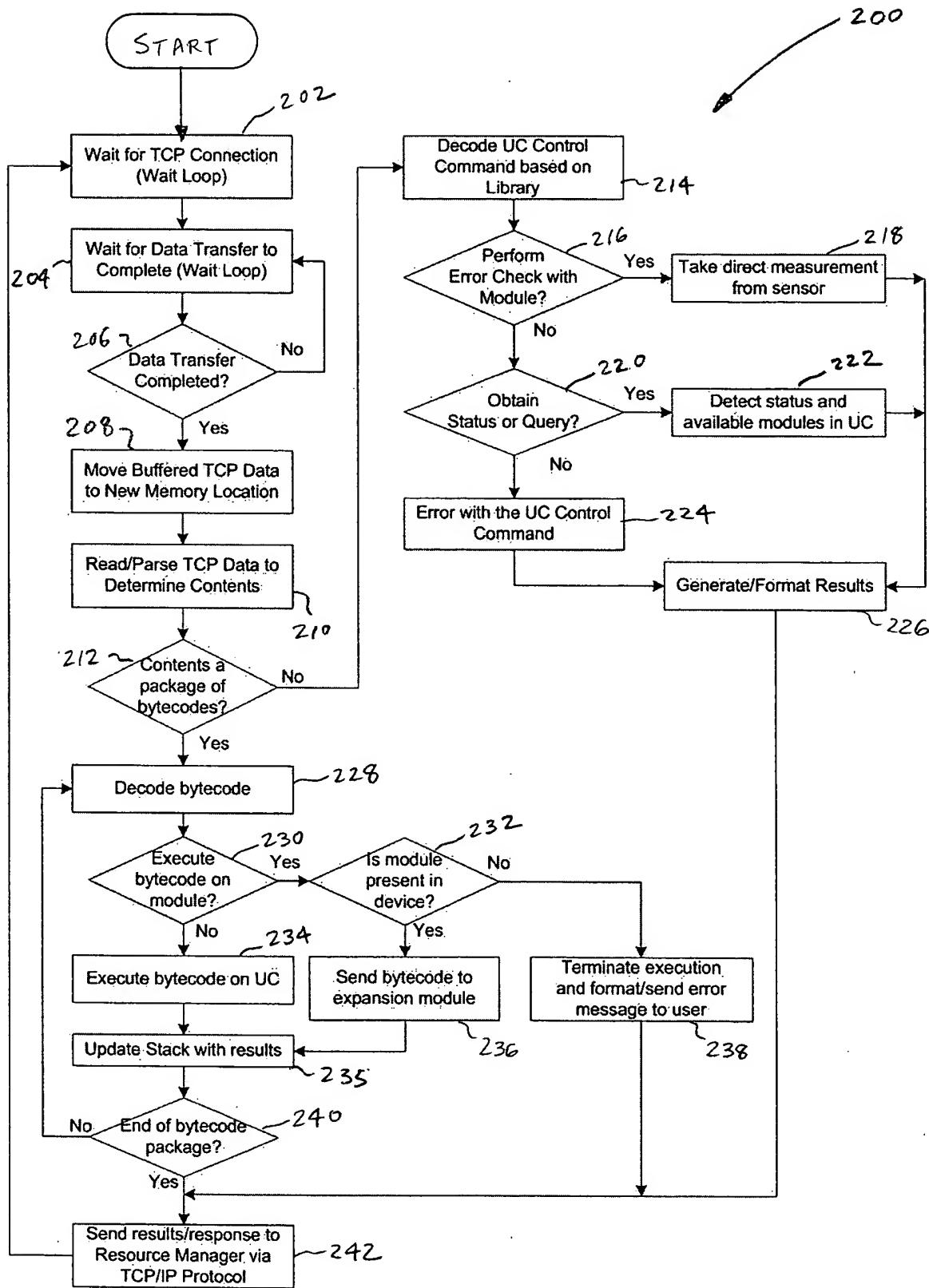


FIG. 7b

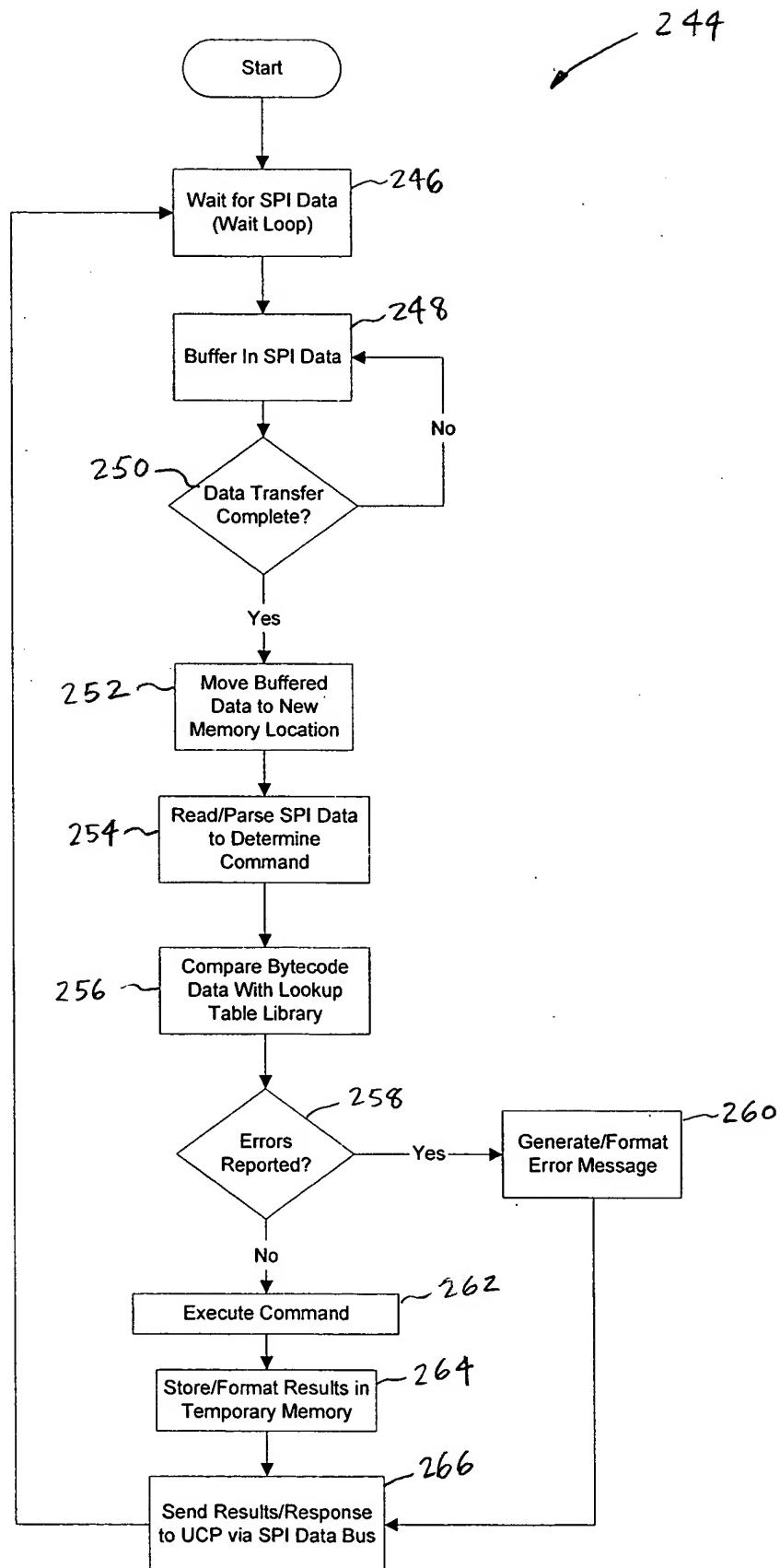


FIG. 8

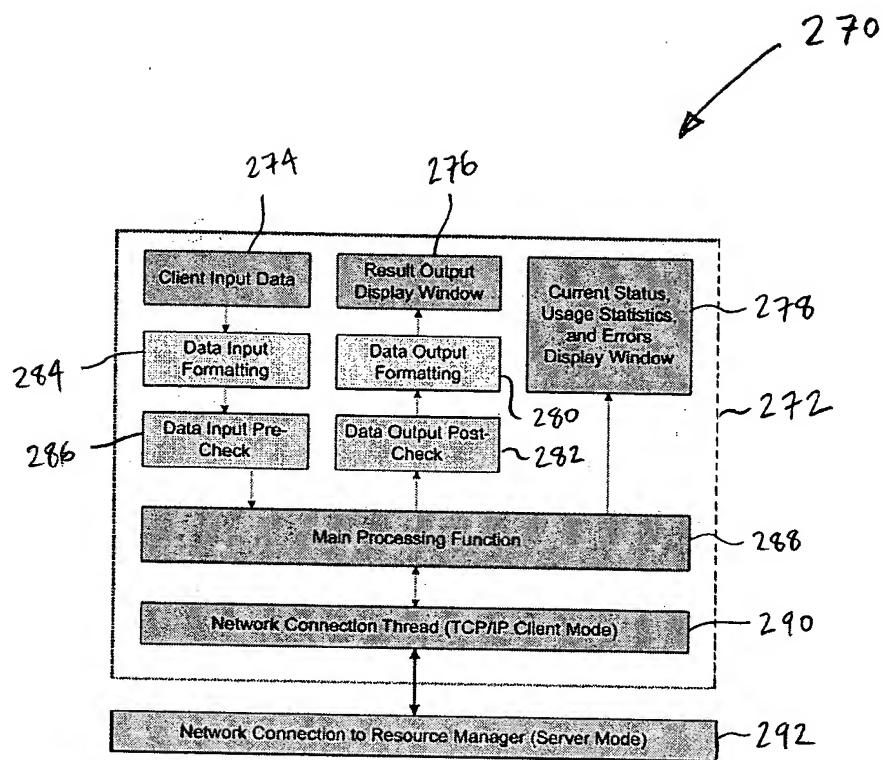


FIG. 9

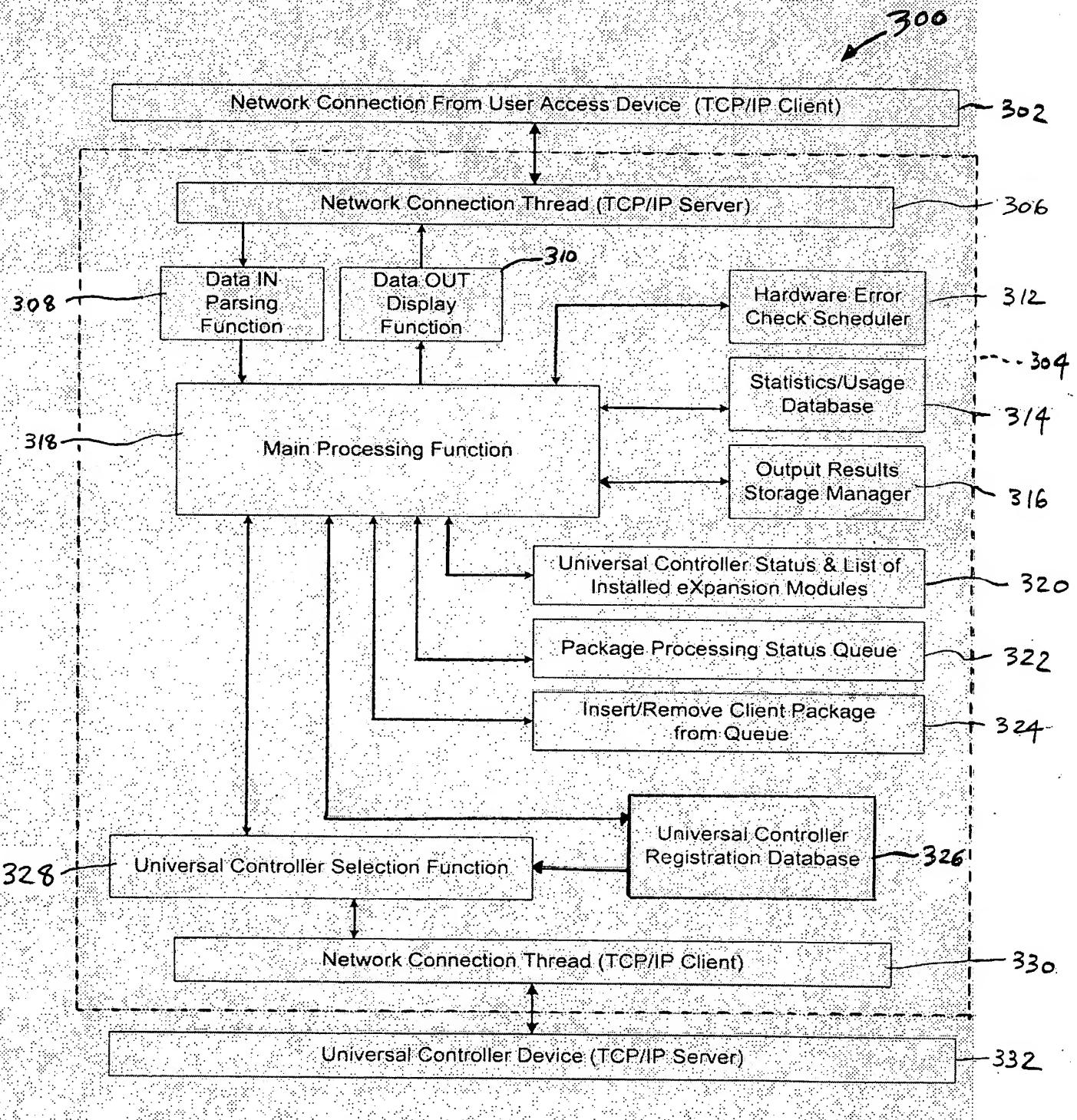


FIG. 10

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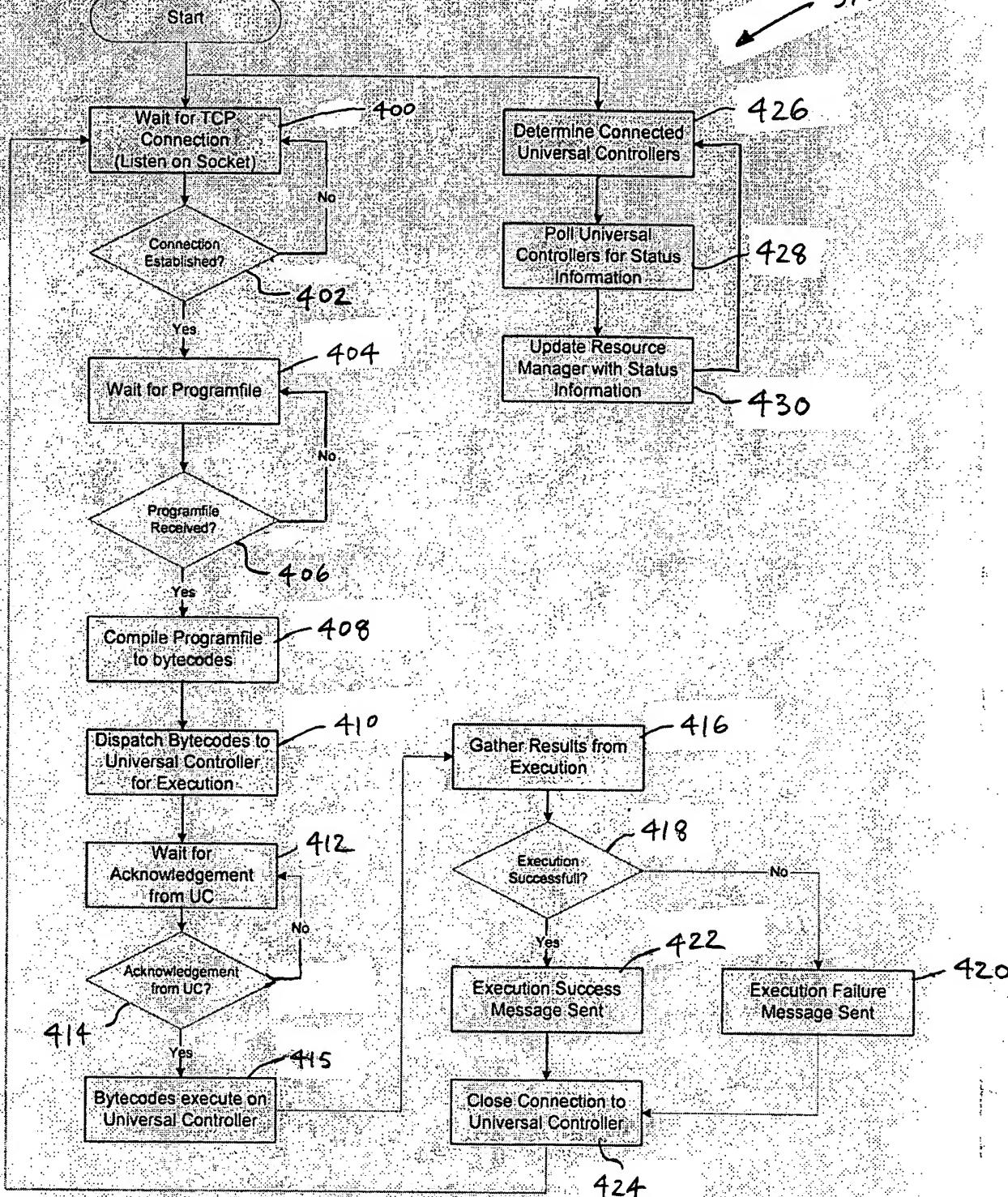


FIG. 11a

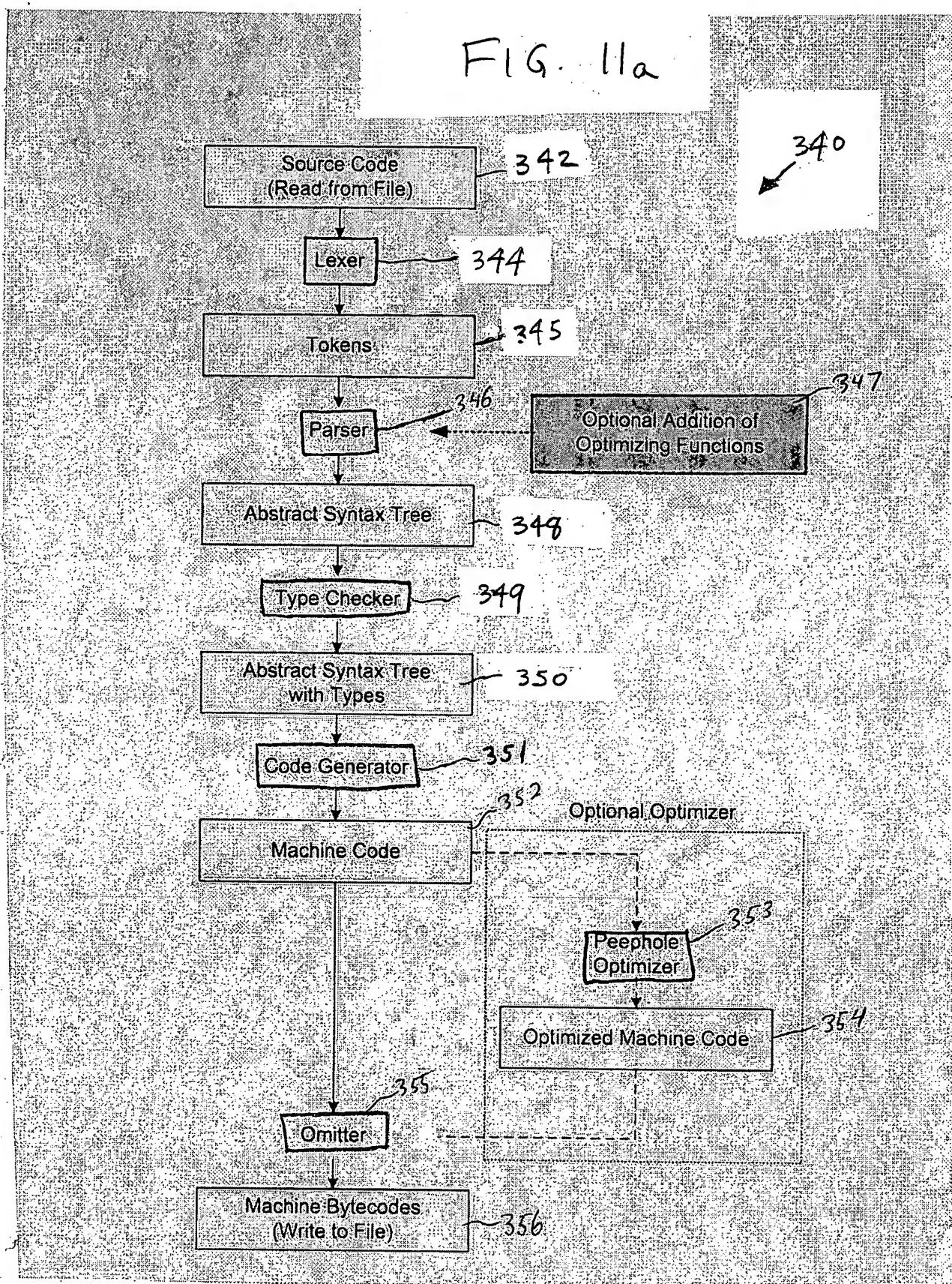


Fig. 11b

ucc (high-level)

```
#define ADC_ADDR 23
#define CH1 1

void main(void)
{
    int x = 78;
    int y = 10;

    while (y <= 0) {
        y = read_adc(ADC_ADDR, CH1);
        y = y + x;
    }
}
```

ucASM (low-level)

```
bipush 78      ;x = 78
istore_1        1010
bipush 10      ;y = 10
istore_2        3E
loop: xmf 23   ;read_adc()
        istore_2        F102032301
        ifle loop      ;if y<=0 loop
        iload_2        9E05
        iload_1        1C
        iadd           1B
        istore_2        60
        end            3E
        end of prog   FF
```

bytecodes

```
1078
3D
1010
3E
F102032301
3E
1C
1B
60
3E
FF
```

FIG. 12

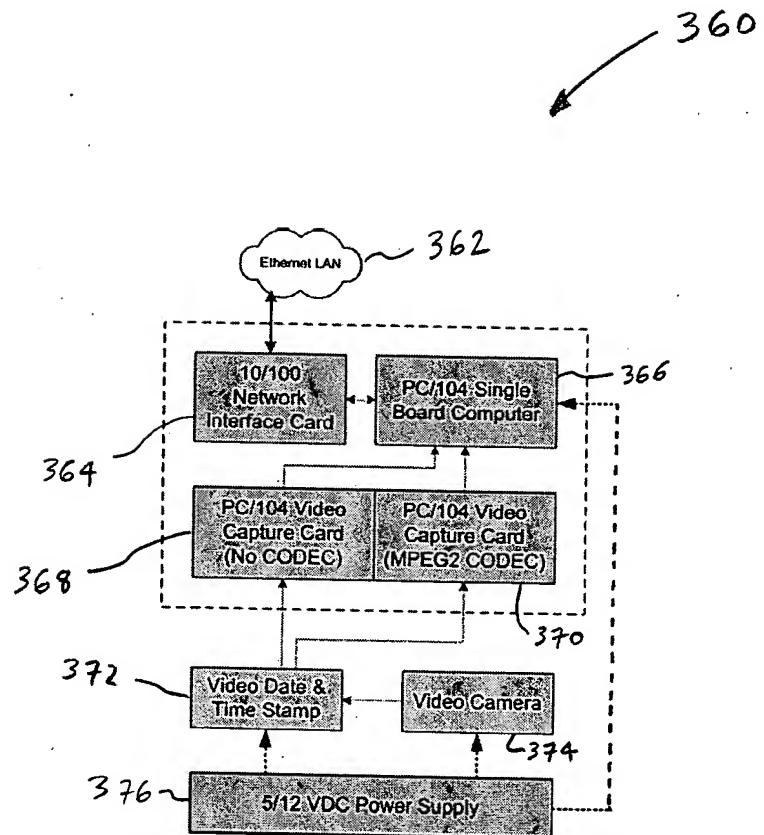


FIG. 13

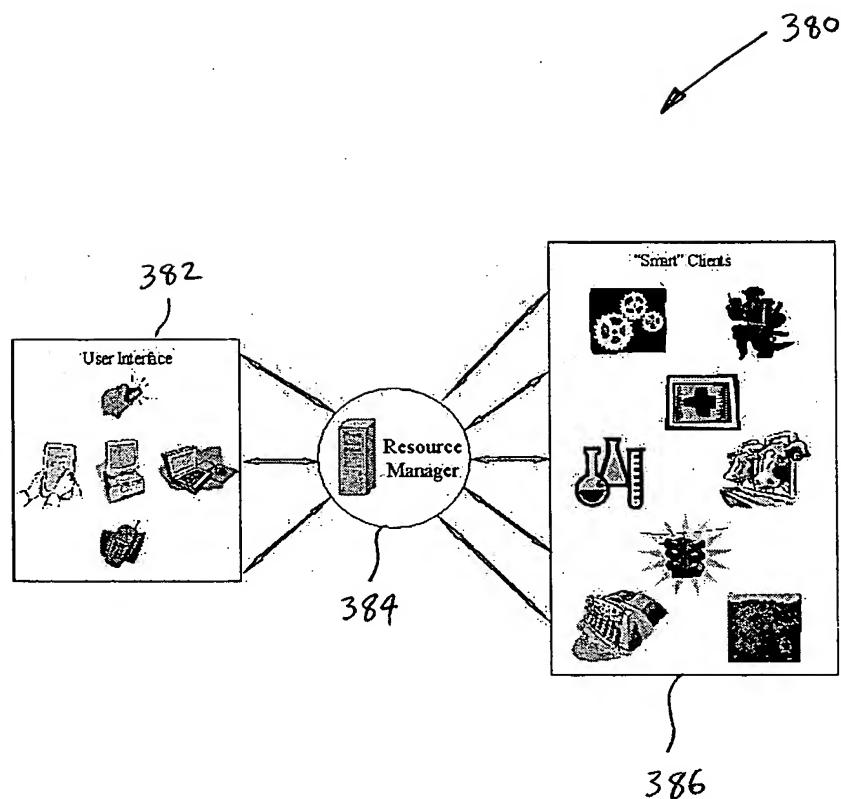


FIG. 14

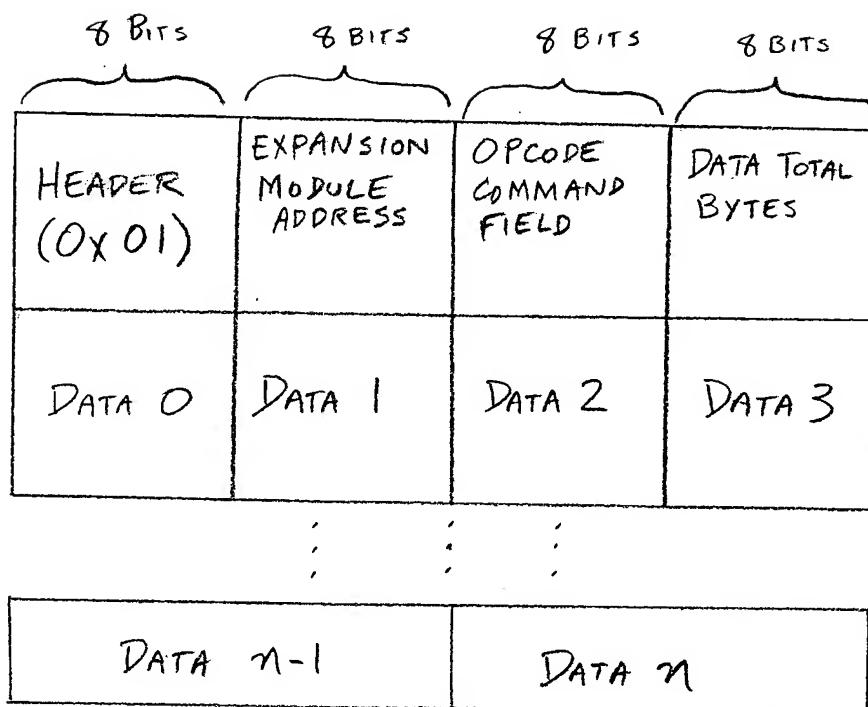


FIG. 15

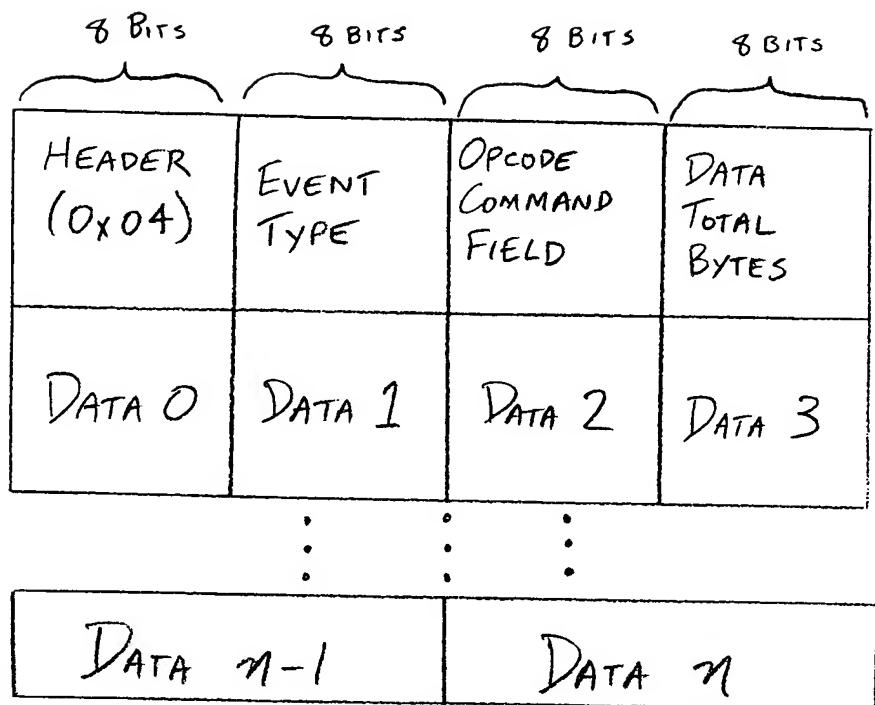


FIG. 16

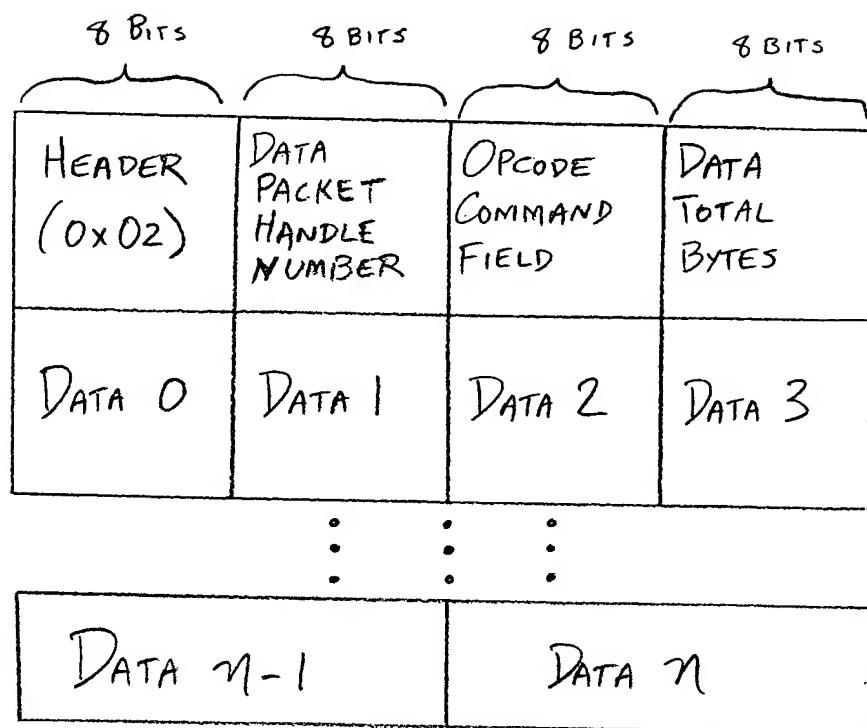


FIG. 17

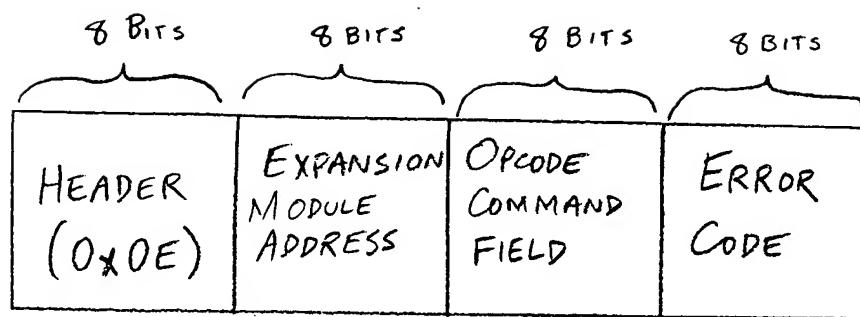


FIG. 18

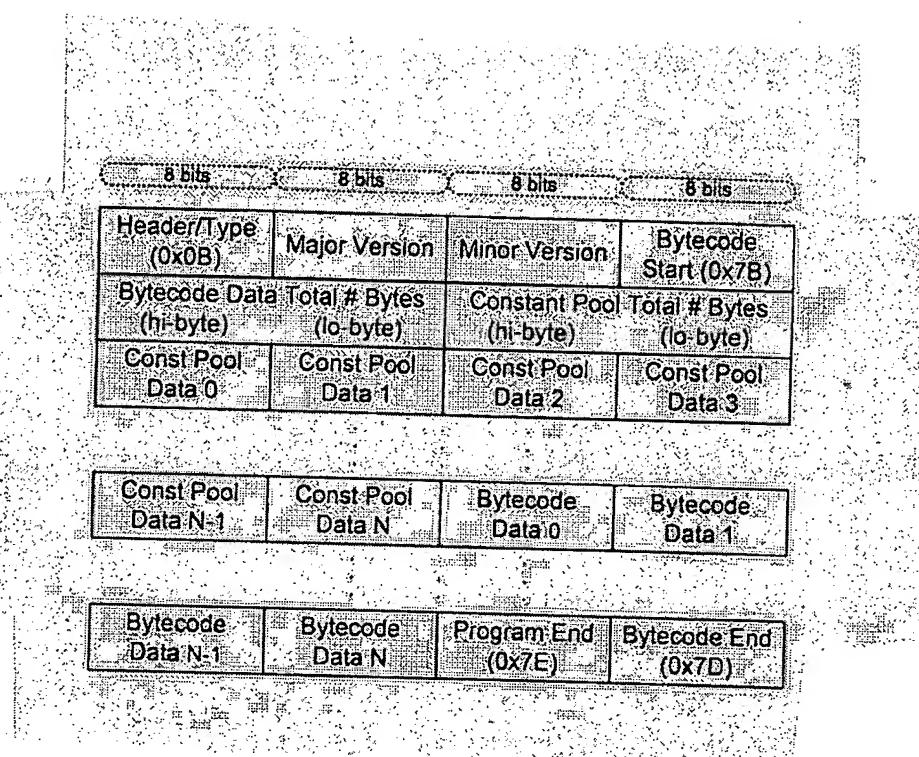


FIG. 19

